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First Named Inventor	Christophe Chevallier
Serial No.	10/017,664
Filing Date	December 12, 2001
Group Art Unit	2812
Examiner Name	Unknown
Confirmation No.	7828
Attorney Docket No.	400.069US01

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Title: FLASH ARRAY IMPLEMENTATION WITH LOCAL AND GLOBAL BIT LINES

Commissioner for Patents
Washington, D.C. 20231

In compliance with 37 C.F.R. §§ 1.56 and 1.97, *et seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified Application.

Applicant respectfully requests that this Supplemental Information Disclosure Statement be entered and the references listed on the attached Form 1449 be considered by the Examiner and made of record.

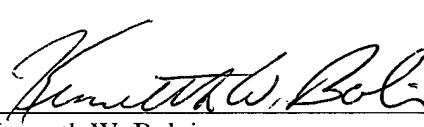
Pursuant to MPEP §609, Applicant further requests that the Examiner initial next to each reference on the Form 1449 to indicate that the listed references have been considered. Applicant further requests that a copy of the initialed Form 1449 be returned with the next official communication.

The enclosed references were recently cited in a communication in a counterpart foreign application. A copy of the International Search Report is enclosed herewith.

As an Office Action has not yet issued in this application, Applicant believes that no fees are due. However, the Commission for Patents is hereby authorized to charge any additional fees to Deposit Account No. 501373. If the Examiner has any questions or concerns regarding this application, please contact the undersigned at the number listed below.

Respectfully submitted,

Date: 4/11/03


Kenneth W. Bolvin
Reg. No. 34,125

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2812

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**GENERAL
TRANSMITTAL
FORM UNDER 37 CFR 1.8
(LARGE ENTITY)**

Commissioner for Patents
Washington, DC 20231

Enclosures

The following documents are enclosed:

A SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT (1 pg.) INCLUDING A FORM 1449 (1 pg.); A COPY OF THE 3 REFERENCES LISTED THEREON; AND A COPY OF THE INTERNATIONAL SEARCH REPORT.

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A AN ITEMIZED RETURN-RECEIPT POSTCARD

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Please charge any additional fees or credit any overpayments to Deposit Account No. 501373.

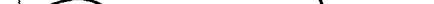
CUSTOMER NO. 27073

Submitted By

Name	Kenneth W. Bolvin	Reg. No.	34,125	Telephone	(612) 312-2211
Signature				Date	4/11/03

Certificate of Mailing

I certify that this correspondence and the identified documents listed on this transmittal are being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231 on April 11, 2003.

Name	Rhonda L. Hutar	Signature	
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REF ID: A12345678901234567890



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**SUPPLEMENTAL
INFORMATION DISCLOSURE
STATEMENT
FORM PTO-1449**

Title: FLASH ARRAY IMPLEMENTATION WITH LOCAL AND GLOBAL BIT LINES

U.S. Patent References				
Examiner Initials	Document No.	Issue/Published Date	Name	Filing Date
	5,777,922	July 7, 1998	Choi	October 18, 1996
	5,894,437	April 13, 1999	Chang	January 23, 1998

Foreign Patent References					
Examiner Initials	Foreign Patent		Name	Publication Date	Translation
	Country	No.			
	WO		Micron Technology, Inc.	10/10/96	

Other References	
Examiner Initials	Author, Title, Date, Pages, etc.
	NONE

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Examiner Signature		Date Considered	
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*Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Based on Form PTO-FB-A820 Patent and Trademark Office, U.S. Department of Commerce